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09866005\_LIST

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PLUS Search Results for S/N 09866005, Searched September 02, 2004

The Patent Linguistics Utility System (PLUS) is a USPTO automated search system for U.S. Patents from 1971 to the present. PLUS is a query-by-example search system which produces a list of patents that are most closely related linguistically to the application searched. This search was prepared by the staff of the Scientific and Technical Information Center, SIRA.

5790831  
5867645  
5974495  
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5721497  
5928343  
5243703  
5319755  
5396602  
5408129  
5473575  
5513327  
5535342  
5546560  
5603043  
5638334  
5657481  
5809263  
5841715  
5841580  
5857109  
5890002  
5915105  
5953263  
5954804  
5983320  
5995443  
6032214  
6032215  
6034918  
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6038195  
6044426  
6049846  
6067592

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6070222  
6085284  
6101152  
6128696  
6182184  
6185644  
4493022  
4511958  
4951283  
5191657  
5237670  
5247642  
5274783  
5313594  
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09866005\_QUAL

5790831 99  
5867645 99  
5974495 99  
5978860 99  
5721497 97  
5928343 97  
5243703 94  
5319755 94  
5396602 94  
5408129 94  
5473575 94  
5513327 94  
5535342 94  
5546560 94  
5603043 94  
5638334 94  
5657481 94  
5809263 94  
5841715 94  
5841580 94  
5857109 94  
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5915105 94  
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5995443 94  
6032214 94  
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6038195 94  
6044426 94  
6049846 94  
6067592 94  
6070222 94  
6085284 94  
6101152 94  
6128696 94  
6182184 94  
6185644 94  
4493022 92  
4511958 92  
4951283 92  
5191657 92  
5237670 92  
5247642 92  
5274783 92

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5313594 92  
5444858 92

09866005\_CLS

Most Frequently Occurring Classifications of Patents Returned  
From A Search of 09866005 on September 02, 2004

Original Classifications

8	710/305
4	365/233
4	710/104
3	710/315
3	711/105
2	365/194
2	710/105
2	713/400

Cross-Reference Classifications

6	365/230.02
5	365/189.02
5	365/233
4	710/3
4	710/51
4	710/61
4	710/9
3	710/36
2	365/194
2	365/230.03
2	710/315
2	711/170
2	713/400
2	713/401
2	713/500

Combined Classifications

9	365/233
8	710/305
6	365/230.02
5	365/189.02
5	710/315
4	365/194
4	710/104
4	710/3
4	710/36
4	710/51
4	710/61
4	710/9
4	711/105
4	713/400
3	365/230.03
3	710/105

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2 710/113  
2 710/260  
2 711/170  
2 713/401  
2 713/500

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09866005 CLSTITLES  
Titles of Most Frequently Occurring Classifications of Patents Returned  
From A Search of 09866005 on September 02, 2004

9 365/233 (4 OR, 5 XR)  
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL  
365/230.01 ADDRESSING  
365/233 .Sync/clocking

8 710/305 (8 OR, 0 XR)  
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA  
PROCESSING SYSTEMS: INPUT/OUTPUT  
710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS  
TRANSACTION PROCESSING)  
.710/305 ..Bus interface architecture

6 365/230.02 (0 OR, 6 XR)  
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL  
365/230.01 ADDRESSING  
365/230.02 .Multiplexing

5 365/189.02 (0 OR, 5 XR)  
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL  
365/189.01 READ/WRITE CIRCUIT  
365/189.02 .Multiplexing

5 710/315 (3 OR, 2 XR)  
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA  
PROCESSING SYSTEMS: INPUT/OUTPUT  
710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS  
TRANSACTION PROCESSING)  
710/305 ..Bus interface architecture  
710/306 ..Bus bridge  
710/315 ...Different protocol (e.g., PCI to ISA)

4 365/194 (2 OR, 2 XR)  
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL  
365/189.01 READ/WRITE CIRCUIT  
365/191 .Signals  
365/194 ..Delay

4 710/104 (4 OR, 0 XR)  
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA  
PROCESSING SYSTEMS: INPUT/OUTPUT  
710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS  
TRANSACTION PROCESSING)  
710/104 .System configuring

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4 710/3 (0 OR, 4 XR)  
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA  
PROCESSING SYSTEMS: INPUT/OUTPUT  
710/1 INPUT/OUTPUT DATA PROCESSING  
710/3 .Input/Output addressing

4 710/36 (1 OR, 3 XR)  
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA  
PROCESSING SYSTEMS: INPUT/OUTPUT  
710/1 INPUT/OUTPUT DATA PROCESSING  
710/36 .Input/Output access regulation

4 710/51 (0 OR, 4 XR)  
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA  
PROCESSING SYSTEMS: INPUT/OUTPUT  
710/1 INPUT/OUTPUT DATA PROCESSING  
710/36 .Input/Output access regulation  
710/51 ..Accessing via a multiplexer

4 710/61 (0 OR, 4 XR)  
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA  
PROCESSING SYSTEMS: INPUT/OUTPUT  
710/1 INPUT/OUTPUT DATA PROCESSING  
710/58 .Input/Output process timing  
710/61 ..Synchronous data transfer

4 710/9 (0 OR, 4 XR)  
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA  
PROCESSING SYSTEMS: INPUT/OUTPUT  
710/1 INPUT/OUTPUT DATA PROCESSING  
710/8 .Peripheral configuration  
710/9 ..Address assignment

4 711/105 (3 OR, 1 XR)  
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING  
SYSTEMS: MEMORY

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711/100 STORAGE ACCESSING AND CONTROL  
711/101 .Specific memory composition  
711/104 ..Solid-state random access memory (RAM)  
711/105 ...Dynamic random access memory

4 713/400 (2 OR, 2 XR)  
Class 713 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING  
SYSTEMS: SUPPORT  
713/400 SYNCHRONIZATION OF CLOCK OR TIMING SIGNALS,  
DATA, OR PULSES

09866005\_CLSTITLES

- 3 365/230.03 (1 OR, 2 XR)  
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL  
365/230.01 ADDRESSING  
365/230.03 .Plural blocks or banks
- 3 710/105 (2 OR, 1 XR)  
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA  
PROCESSING SYSTEMS: INPUT/OUTPUT  
710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS  
TRANSACTION PROCESSING)  
710/105 .Protocol
- 2 710/113 (1 OR, 1 XR)  
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA  
PROCESSING SYSTEMS: INPUT/OUTPUT  
710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS  
TRANSACTION PROCESSING)  
710/107 .Bus access regulation  
710/113 ..Centralized bus arbitration
- 2 710/260 (1 OR, 1 XR)  
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA  
PROCESSING SYSTEMS: INPUT/OUTPUT  
710/260 INTERRUPT PROCESSING
- 2 711/170 (0 OR, 2 XR)  
Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING  
SYSTEMS: MEMORY  
711/100 STORAGE ACCESSING AND CONTROL  
711/170 .Memory configuring
- 2 713/401 (0 OR, 2 XR)  
Class 713 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING  
SYSTEMS: SUPPORT  
713/400 SYNCHRONIZATION OF CLOCK OR TIMING SIGNALS,  
DATA, OR PULSES  
713/401 .Using delay
- 2 713/500 (0 OR, 2 XR)  
Class 713 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING  
SYSTEMS: SUPPORT  
713/500 CLOCK, PULSE, OR TIMING SIGNAL GENERATION OR  
ANALYSIS